AMENDMENTS TO THE CLAIMS:

This listing of claims will replace all prior versions, and listings, of claims in the application:

Listing Of Claims:

1-16. (Canceled)

17. (New) A switching regulator, comprising:

a switching device for generating a pulsed signal from an input signal as a function of a switching signal;

a filtering device for filtering the pulsed signal and for outputting a smoothed output signal;

a feedback device;

a controllable amplifier device for generating the switching signal from a reference signal and an actual value signal obtained from the smoothed output signal via the feedback device as a function of a compensation signal; and

a compensation-signal generating device for generating the compensation signal from the input signal.

18. (New) The switching regulator as recited in Claim 17, wherein:

the switching regulator is a step-down transformer.

19. (New) The switching regulator as recited in Claim 17, wherein:

the amplifier device includes a complex grounded resistor, for adjusting at least one of a primary gain and a frequency compensation.

20. (New) The switching regulator as recited in Claim 17, wherein:

the filtering device includes a low-pass filter.

21. (New) The switching regulator as recited in Claim 20, wherein:

the low-pass filter includes an inductance and a capacitance.

22. (New) The switching regulator as recited in Claim 17, further comprising:

a diode is connected in parallel to the filtering device in order to protect the filtering device.

23. (New) The switching regulator as recited in Claim 17, further comprising:

a resistor network including a voltage divider having essentially ohmic resistors and connected to the amplifier device via the feedback device.

24. (New) The switching regulator as recited in Claim 17, wherein:

the controllable amplifier device includes a pulse-width modulating device for generating a pulse-width modulated signal, corresponding to the switching signal,, from an oscillator signal and an amplifier signal.

25. (New) The switching regulator as recited in Claim 24, wherein:

the oscillator signal has a delta voltage-shaped curve.

26. (New) The switching regulator as recited in Claim 17, wherein:

the compensation signal is a current signal.

27. (New) The switching regulator as recited in Claim 17, wherein:

the switching device includes a MOSFET transistor.

28. (New) The switching regulator as recited in Claim 17, wherein:

the input signal includes a quasi-constant battery voltage.

29. (New) The switching regulator as recited in Claim 17, further comprising:

a circuit arranged between the pulse-width modulating device and the switching device, the circuit including an additional amplifier device.

30. (New) A switching regulation method, comprising:

generating a compensation signal from an input signal in a compensation-signal generating device;

generating a switching signal from a reference signal and an actual value signal obtained from the output signal via a feedback device as a function of the compensation signal in a controllable amplifier device;

generating a pulsed signal from the input signal as a function of the switching signal in a switching device; and

filtering the pulsed signal in a filtering device and outputting a smoothed output signal.

31. (New) The method as recited in Claim 30, further comprising:

generating an amplifier signal via a complex resistor connected to the controllable amplifier device.

32. (New) The method as recited in Claim 30, further comprising:

supplying the smoothed output signal to the controllable amplifier device via a resistor network, including a voltage divider in particular provided with ohmic resistors, and the feedback device.

33. (New) The method as recited in Claim 30, further comprising:

generating a pulse-width modulated signal corresponding to the switching signal from an oscillator signal and the amplifier signal in the controllable amplifier device in a pulse-width modulating device.

34. (New) The method as recited in Claim 33, further comprising:

amplifying the pulse-width modulated signal in an additional amplifier device before the switching device is triggered.